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| <b>Product Name :</b><br>Study of Shift Register and Counter                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  | <b>Product Code :</b><br>NLAB-ELECTRONICSAB220036 |
| <b>Description :</b><br>Study of Shift Register and Counter                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |                                                   |
| <b>Technical Specification :</b><br>SCOPE OF LEARNING:<br><br>4 Bit Counter Using D Flip<br><br>4 Bit Shift Register Using JK Flip Flop<br><br>TECHNICAL SPECIFICATIONS:<br><br>Power Supplies:<br><br>DC Power Supply +5V,150mA<br><br>Operated on Mains power 230V, 50Hz +10%<br><br>Technical specifications:<br><br>Two independent logic level inputs to select High / Low TTL levels, each with a LED to indicate high / low status and termination.<br><br>Four independent buffered logic level indicators for High / Low status<br><br>D Flip Flop given on board.<br><br>JK Flip Flop given on board.<br><br>Clock Generator Mono Pulsar given on Board.<br><br>SALIENT FEATURES:<br><br>Front panel built with high class insulated Printed Circuit Board sheet with well printed circuits and symbols.<br><br>Instruction manual. |                                                   |

Connections are brought out through 2mm Sockets.

Patch Cords 2mm.

The trainer is housed in ABS Plastic cabinet/Wooden.

Size of the trainer set 12"x8"

## Naugralabequipments

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